

NEW DESIGN PARADIGMS

IMPROVING QUALITY & CUTTING TIME GOING FROM CONCEPT TO HARDWARE

26-28 June, 2001 8.30 am - 5.00 pm

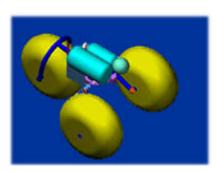
Doubletree Hotel Pasadena-Plaza Las Fuentes, 191 N. Los Robles Ave., Pasadena, CA 91101 Tel:1-626-792-2727 Fax:1-626-792-3755

The workshop will take a systems perspective and look at the full concept to hardware design cycle, across disciplines, and across industries. It will include a mix of talks, panel discussions, discussion groups, and tours of selected facilities at JPL. The workshop will feature prominent speakers from:

> NASA, JPL, ESA, NASDA, Lockheed Martin, Boeing, Astrium, MELCO, MIT, USC and GMU.

For more information, on-line registration, Abstract and presentation submission, go to:

http://nsd2001.jpl.nasa.gov



PROGRAM COMMITTEE Dr. Knut I. Oxnevad, JPL, Chairman Dr. Pat Patterson, NASA HQ Dr. Richard Evans, RGI Paul Gowler, JPL Madhu Thangavelu, USC Dr. Robert Belie, Lockheed Martin

PRESENTATIONS IN THE FOLLOWING AREAS ARE ENCOURAGED

METHODOLOGIES

- Concept Formulation
- Concurrent Design
- Concurrent Engineering
- Virtual Manufacturing

ENABLING TECHNOLOGIES

- STEP
- Rapid Prototyping
- Internet/Intranet/WWW
- Immersive Technologies/Virtual Reality (VR)
- High Performance Computing
- Unified Datamodels
- Product Data Management (PDM)
- Integrated CAD analyses/simulation tools

CROSS INDUSTRIAL EXPERIENCES

- Automobile
- Spacecraft
- Oil and Gas Well Analysis and Design
- Front Loaders (Caterpillar)
- Software
- Amusement Park
- Aircraft

- Evolution of Design/Lessons Learned (Viking, Saturn, Sojourner)
- Virtual Reality Design, Analysis, and Manufacturing; The Future
- The Limitations of VR in the design process





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